



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Haruko INOUE et al.) .
Serial No. 09/666,156)
Patent No 6,784,490 B)Art Unit: 2811
Issued: August 31, 2004)Examiner: Steven Ho Yin LOKE
Filed: September 19, 2000)Confirmation No. 9764
For: HIGH VOLTAGE MOS TRANSISTOR)

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents

Alexandria, VA 22313-1450

Sir:

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In reviewing the above-identified patent, printing errors were discovered thereions to conform the Official Record in the application. requiring correction in order to conform the Official Record in the application.

The errors noted are set forth on the attached copy of Form PTO-1050 (Rev. **3**-82) in the manner required by the Commissioner's Notice.

Specifically, upon reviewing the front page of the patent, it was noted that column 18, claim 2, line 19 incorrectly says "or"; please change to -- of --

The change requested herein occurred as a result of printing the Letters Patent and the Certificate should be issued without expense under Rule 322 of the Rules of Practice. Accordingly, applicants request issuance of the Certificate of Correction

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,784,490 B) : August 31, 2004 **DATED** INVENTOR(S): Haruko INOUE et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby

corrected as shown below:

Column 18, claim 2, line 19; please change the word "or" to -- of --

MAILING_ADDRESS OF SENDER:

PATENT NO. ____ No. of additional copies

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> SEND TO: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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